MARK SCHEME for the October/November 2011 question paper

for the guidance of teachers

9691 COMPUTING

9691/33

Paper 3 (Written Paper), maximum raw mark 90

This mark scheme is published as an aid to teachers and candidates, to indicate the requirements of the examination. It shows the basis on which Examiners were instructed to award marks. It does not indicate the details of the discussions that took place at an Examiners' meeting before marking began, which would have considered the acceptability of alternative answers.

Mark schemes must be read in conjunction with the question papers and the report on the examination.

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	Page 2	Mark Scheme: Teachers' version	Syllabus	Paper	
		GCE A LEVEL – October/November 2011	9691	33	
1	(a) -A short -Stored -availab -Used to (1 per -,	sequence of machine code instructions in the ROM le when the power is switched on load the operating system max 2)		[2]	
	(b) -Power- -Carries -Uses th -Mentior -First ins (1 per -,	 Power-up process places address of first instruction in PC Carries out the POST (power on self-test) Uses the user-defined parameters in the boot file to configure the system Mention of BIOS/autoexec.bat/config.sys First instruction of OS is loaded into memory and address passed to PC (1 per -, max 4) 			
2	(a) -during t	he fetch stage the contents of the address in the MAR	is copied into the	e MDR	

- -during the fetch stage the contents of the address in the MAR is copied into the MDR ...
 -The contents of MDR is the instruction to be used (this is copied to the CIR)
 -during the execute stage (store instruction) the contents of the ACC is copied into the MDR
 -during the execute stage (load/add instruction) the contents of the memory location is copied into the MDR
 (1 per -, max 3)
 - (b) -Data bus carries contents of a memory location/contents of a register/a data value/an address/an instruction

-Address bus carries an address of a memory <u>location/device</u> -the address bus carries an address from the processor to main memory/a device

-Control bus

- Separate wires each used to carry a control signal
- the bus carries control signals to the various components
- by example e.g. read operation completed // interrupt

-Data bus is bi-directional // data bus used to read/write data // Address bus is uni-directional // the control bus is bi-directional

(mark as 3 × 2 per bus, max 6)

[6]

Page 3		3	Mark Scheme: Teachers' version Syllabus		Paper	
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3	(a)	(i)	+39	= 00100111		
		(ii)	–47 (1 fc	= 10101111 or both sign bits and 1 for each magnitude part)		[3]
	(b)	(i)	-3 =	1111 1101		[2]
		(ii)	-47	= 1101 0001		[2]
			(in e	ach case 1 mark per nybble)		
	(c)	(i)	= (1) = 5/3= 21/OR:= 0.0Hen= 10	$\frac{\frac{8 + 1/32}{32 \times 16}}{\frac{2}{2}} \times \frac{2^{4}}{2}$ $\frac{20101}{2} \times \frac{2^{4}}{2}$ ce move point 4 places $0.1 = 2 \frac{1}{2}$		
			(1 m	hark for each underlined section, max 2. Note: Accept of	lecimal values)	[2]
		(ii)	010 [.] (1 fc	100 0010 or mantissa, 1 for exponent)		[2]
		(iii)	M = E = Num = <u>6 1</u>	<u>1⁄2 + 1⁄4 + 1/32</u> OR = <u>25/32</u> 3 <u>3</u> Nber is 25/32 * 8 1⁄4		
			(1 pe	er line, max 3)		[3]
4	(a)	 (a) -networked communication system// content provided by a web server -probably provided on the Internet -Restricted access -to specific members authorised by the health system -Access is password controlled -Content viewed using browser software 				
		(1 p	oer -,	max 3)		[3]
	(b)	Adv -Lir -Inf -Le -ea Dis -ma -ex	vanta nited orma eds p orma ss inf sier to advar ay inv tra ad	ges number of users speeds up access tion being communicated is sensitive/confidential protection from being seen by unauthorised people tion on system will be relevant/easily updated ormation makes it easier to navigate o control who can access the content ntages olve additional set-up costs // need to set up a LAN lministration // setting up users (& passwords)/access r	rights	[5]
		(' '	-, ioi			[0]

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5 (a) -Cost of creating the real thing == different braking units would need to be built -Time that would be needed to create the real thing == the parameters of the simulated

braking system can be changed immediately

-Time taken to run the tests == test time can be greatly reduced

-a wide variety of conditions need to be tested == e.g. it may be necessary to drive for 100000 miles/at different speeds, this could be simulated

-Ability to change conditions immediately == e.g. not necessary to transport car to different parts of the world

-extreme case scenarios can be tested == conditions may never occur during real life testing (3 x 2 per advantage) [6]

(b) -Speed

-in order to simulate stopping distances

-Weight of car and load...

-in order to simulate the effects of inertia with different loads

-Materials used/size of structure used/methods of fixing to car

-to try to reduce final cost while still remaining efficient

- -driving style
- -gentle braking/hard braking/cornering/reaction time

-tyres

-wear/type of tread/tyre material

-road surface

-roughness/material

-weather conditions (temperature, wind, precipitation)

-in order to replicate different climates...

(1 per -, max 5)

[5]

- 6 (a) -Reduces repetition/duplication of data items // keeps physical volume of data to a minimum // minimises redundant data

 Increases data integrity // reduces data inconsistency
 Simpler data retrieval through queries // reports are easy to generate
 Amending/searching/sorting data is easier
 Amending the data structures is simpler to implement
 Changes to the data structure will not affect existing applications programs // Program/data independence

 (1 per -, max 3)

 (b) (i) -GuestID or similar

 unique

 (2]
 - -so that items can be accessed according to a different criteria other than by primary key [2]
 - (iii) -Attribute/field in one table which links to the primary key in another table [1]
 - (iv) -GuestID... -to link each account to the relevant guest // to link ACCOUNT and GUEST tables [2]

	Page 5		Mark Scheme: Teachers' version	Syllabus	Paper		
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7	(a) -du -ide -ch -Er -va -Da -Da -du -du -as (1 p	ring le eckec ror me riable ata typ dress ring s signm per -,	exical analysis rs and keywords are differentiated I against rules (e.g. length) for identifiers essages produced if identifier does not match the expe identifiers will be tokenised identifiers entered into symbol table be will be added to the entry in the symbol table es in memory allocated to variables yntax/semantic analysis stage tent of illegal types of data to variables is reported max 7)	cted rules	[7]		
	(b) (i)	-obje -obje -con -the -Cor -Cor (1 pe	ect code is difficult to interfere with ect code runs faster than interpreted source code apiler can optimise executable code code is not translated each time the program is run apiler does not need to be present when the program i apiled code will be free from syntax errors er -, max 2)	s run	[2]		
	(ii)	-Erro -repo -stop -Par writte -erro (1 pe	ors are <u>more</u> easily located orts errors when source code is present oping at the point of the error ts (only) of program can be tested/testing can be sta en ors when found can be immediately corrected. er -, max 2)	rted before all t	he program is [2]		
 8 Paging memory is divided into equal-sized units called page frames program/data file is divided into equal-size units called pages one or more pages may be loaded into memory at any one time Pages may be discontiguous Pages swapped in and out as required pages not in main memory are stored in virtual memory/backing store page table/Index of pages/processes kept absolute address is calculated by adding page address to relative address in paging is transparent to the programmer Segmentation Memory is divided into variable length blocks Programs can consist of many segments 				ore address in instru	ction		
	-Segments normally match natural divide in jobs/logical blocks -Index of segments stored which must						

-store base address and length of segment

-programmer will organise code modules into segments

(1 per -, max 4 per dotty, max 6)

	Page 6			Mark Scheme: Teachers' version	Syllabus	Paper
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9	(a)	(i)	-describes what is to be accomplished -not how (no algorithm written) -the user states what is to be found/set a goal -Consists of a set of facts and rules -Rules are applied to the data until the goal is reached -Mention of backtracking/instantiation (max 1)			
		(ii)	-Pro	gram describes how to solve the problem in a sequence	ce of steps/algor	ithm
			-lend	ds itself to top-down design/modularisation		
			(max	(1)		[2]
			•	·		
	(b)	(i)	-A class is the "blueprint" from which objects are defined // a class consists of the properties and methods that define each object			
			-1 101			[2]
		(ii)	-One -Tree	e class can use the properties and methods from a par e/Bulb inherits the properties and methods of Plant	ent/base/super	class [2]
	((iii)) -An object can only read/write a property value using methods of th contains both properties and the methods to use it			class // Class
			-e.g. meth	The CountryOfOrigin property can only be output nod in the class Tree	using the getC	ountryOfOrigin [2]
10	(a)	(i)	Mus	t begin with at least one <letter></letter>		
		(ii)	X is	not defined as a <letter></letter>		
	((iii)	A ma	aximum of 2 digits is allowed at the end		[3]





Mark Points:

-Only one entry and one exit point used -Order correct (letter, non-zero-digit, digit) -Loop around **letter** -alternative path to omit number -alternative path to omit 2nd digit (1 per -, max 4)

[4]